

## Patent Abstracts of Japan

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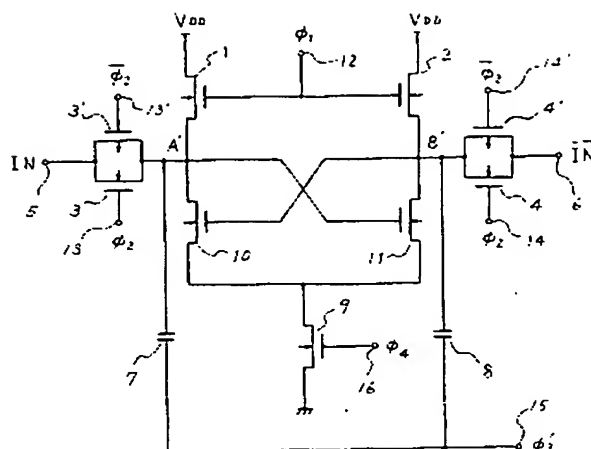
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TITLE : DRIVING METHOD OF DETECTING  
AMPLIFYING CIRCUIT



ABSTRACT : PURPOSE: To prevent the occurrence of substrate currents and to secure stable operations of the detecting amplifying circuit, by cutting off the boosting means during the period where nodal points are charged through pre-charging means.

CONSTITUTION: Pre-charging means 1, 2 which charge nodal points A', B', operating means 3, 3' and 4, 4' which couple input signals for a necessary period, boosting means 7, 8 which supply voltages to the nodal points A' and B' under a capacitive coupling, and an activating means 9 which controls the flip flop operatin, are installed. It is arranged that, when such a detecting amplifying circuit is driven, the boosting means 7, 8 are cut off during the period where the nodal points A', B' are charged through the precharging means 1, 2. For example, in the circuit shown in the diagram, the voltage of the nodal points A', B' is changed by giving a pre-charging clock  $\phi_1$ , a transferring clock  $\phi_2$ , a boosting clock  $\phi_3$ , and an activating clock  $\phi_4$  at an adequate timing.

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